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TC # 640

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10008965	11/13/2001	439	138	2839	PRASAD

**APPLICANTS: Genduso Thomas; Pase Douglas;

**CONTINUING DATA VERIFIED: none cfd

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** FOREIGN APPLICATIONS VERIFIED: none cfd

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PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		RPS920010086US1
Verified and Acknowledged Examiners's initials <i>cfb</i>		
TITLE : Apparatus for interconnecting electronic components		

U.S. DEPT. OF COMM./PAT. & TM.-PTO-4321 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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